

VELAGAPUDI RAMAKRISHNA
SIDDHARTHA ENGINEERING COLLEGE:: VIJAYAWADA - 7
(AUTONOMOUS)

Date: 03/01/2022

**LIST OF REGD. NO. OF SUCCESSFUL / BENEFITTED CANDIDATES BY WAY OF
REVALUATION OF B.TECH. II SEMESTER REGULAR & I SEMESTER SUPPLE
(VR20 REGULATION) EXAMINATIONS HELD IN OCTOBER -2021**

20BS2101 - Laplace Transforms & Integral Calculus

208W1A0545 208W1A04G9 208W1A10C0 208W1A1246

20ES2103A - Object Oriented Programming Using Python

208W1A0488 208W1A1214

20ES2103B - Python Programming

208W1A0131 208W1A1053

20ES2104A - Basics of Electronics Engineering

208W1A05A8 208W1A05C4 208W1A12C2

20ES2104B - Circuit Analysis

208W1A04K8

20ES2104C - Network Theory

- NIL -

20ES2104D - Engineering Mechanics

- NIL -

20ES2104E - Network Analysis

- NIL -

20ES2104F - Engineering Mechanics-II

208W1A03A2

20HS2105 - Technical English & Communication Skills

208W1A0178

20BS1101 - Matrices & Differential Calculus

208W1A1053

20BS1102/20BS2102 - Engineering Chemistry

208W1A0513 208W1A04J4 208W1A1092

20BS1102A/20BS2102A - Engineering Physics

208W1A1044 208W1A0215

20ES1103- Programming for Problem Solving

- NIL -

20ES1104 - Basics of Electrical Engineering

- NIL -

20ES1104B - Mechanics For Engineers

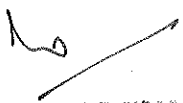
- NIL -

20ES1104C - Engineering Mechanics -I

- NIL -

20ES2105 - Engineering Graphics

208W1A0521 208W1A0498 208W1A04P6 208W1A1261


CONTROLLER OF EXAMINATIONS
Velagapudi Rama Krishna
SIDDHARTHA ENGINEERING COLLEGE
(AUTONOMOUS)
VIJAYAWADA-520 007.A.P.


PRINCIPAL
V.R. Siddhartha Engineering College
AUTONOMOUS
VIJAYAWADA-520 007