

**VELAGAPUDI RAMAKRISHNA
SIDDHARTHA ENGINEERING COLLEGE:: VIJAYAWADA - 7
(AUTONOMOUS)**

Date: 21/11/2024

**LIST OF REGD. NO. OF SUCCESSFUL / BENEFITTED CANDIDATES BY WAY OF
REVALUATION OF B.TECH. I & II SEMESTER SUPPLEMENTARY EXAMINATIONS
(VR17 REGULATION) HELD IN OCTOBER -2024**

17CS1103 – Problem Solving Methods

- NIL -

17EC1204A – Basic Electronic Engineering

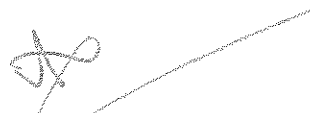
- NIL -

17EE1204 – Network Analysis -I

- NIL -


CONTROLLER OF EXAMINATIONS

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PRINCIPAL

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**LIST OF REGD. NO. OF SUCCESSFUL / BENEFITTED CANDIDATES BY WAY OF
REVALUATION OF B.TECH. III SEMESTER SUPPLEMENTARY EXAMINATIONS
(VR17 REGULATION) HELD IN OCTOBER -2024**

17CE3302 - Introduction to Mechanics of Solids

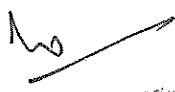
- NIL -

17CE3303 – Fluid Mechanics

- NIL -

17ME3305 – Kinematics of Machines

- NIL -



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17ME2504A - Robotics

- NIL -


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**LIST OF REGD. NO. OF SUCCESSFUL / BENEFITTED CANDIDATES BY WAY OF
REVALUATION OF B.TECH. VII SEMESTER SUPPLEMENTARY (VR17 REGULATION)
EXAMINATIONS HELD IN OCTOBER -2024**

17EE4704B –VLSI Design

- NIL -


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